

PTO  
09/773595  
03/03/01

Class	Subclass

PATENT NUMBER

5-81  
1876

# U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
TR SCANNED <i>MA</i> Q.A. <i>AC</i>	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/773595	F	713		2182	<i>ee Du</i>

APPLICANTS  
Atsushi Yoshikawa  
Yasuhiko Hagihara

2-16

TITLE Delay adjustment circuit and a clock generating circuit using the same

PTO-2040  
12/99

ISSUING CLASSIFICATION							
ORIGINAL CLASS		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

☐ Continued on Issue Slip inside File Jacket

<b>TERMINAL DISCLAIMER</b>  <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____.  <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<b>DRAWINGS</b> Sheets Drwg.    Figs. Drwg.    Print Fig.			<b>CLAIMS ALLOWED</b> Total Claims    Print Claim for O.G.	
	_____ (Assistant Examiner)    _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner)    _____ (Date)			<b>ISSUE FEE</b> Amount Due    Date Paid	
	_____ (Legal Instruments Examiner)    _____ (Date)			<b>ISSUE BATCH NUMBER</b>	

**WARNING:**  
The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

Form PTO-438A  
(Rev. 6/99)

FILED WITH: ☐ DISK (CRF) ☐ FICHE ☐ CD-ROM  
(Attached in pocket on right inside flap)

BEST AVAILABLE COPY